

FIG.1

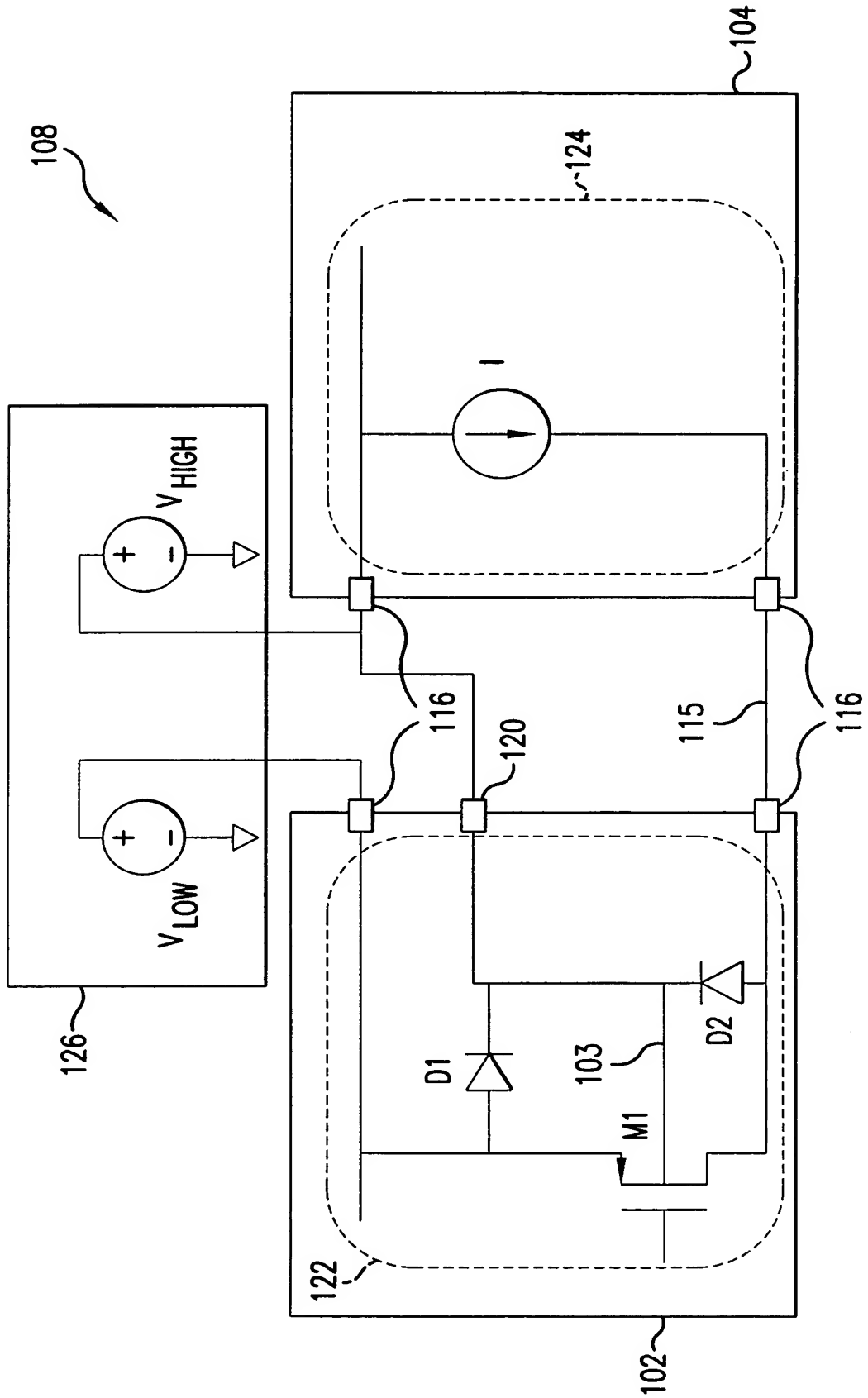
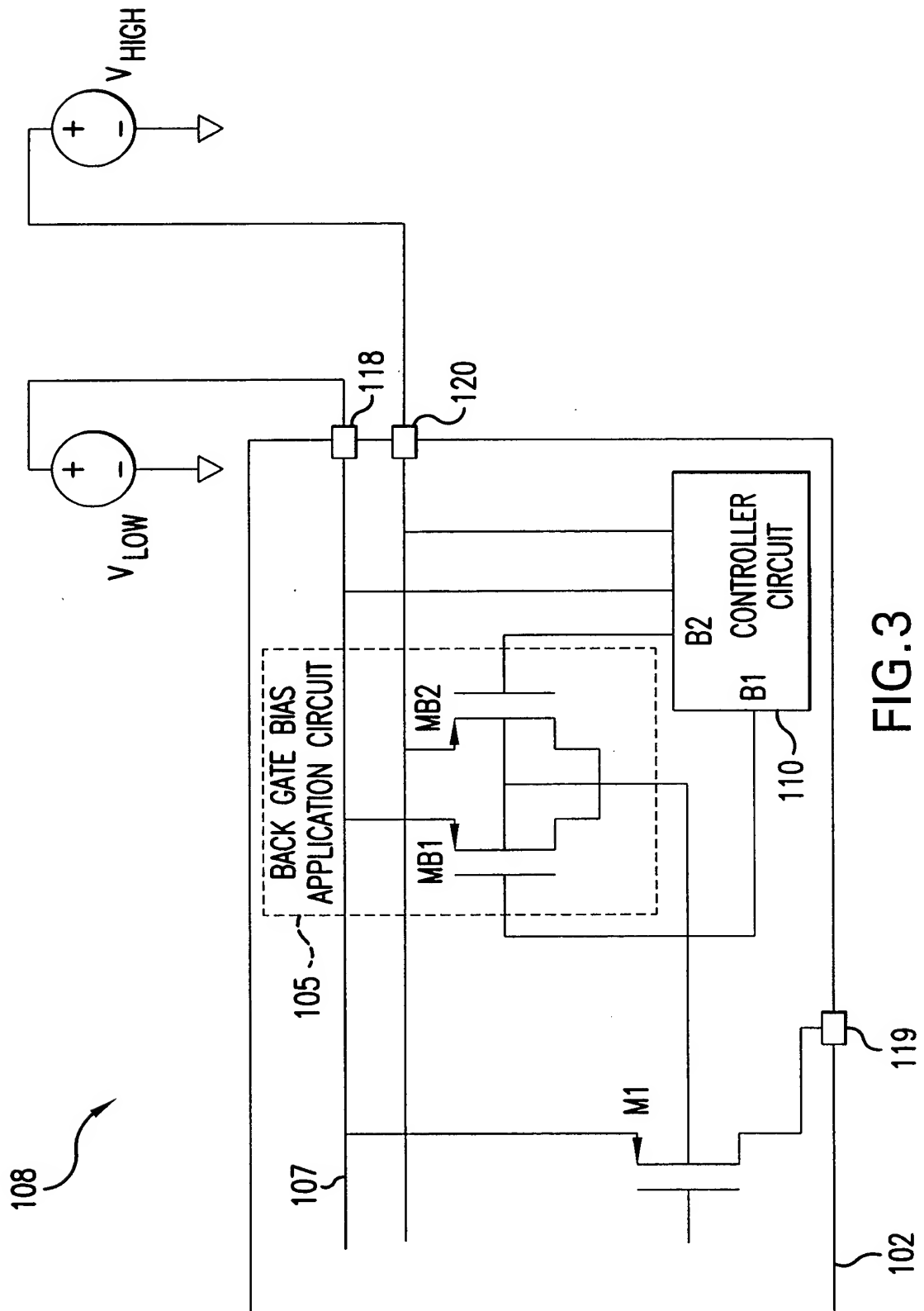
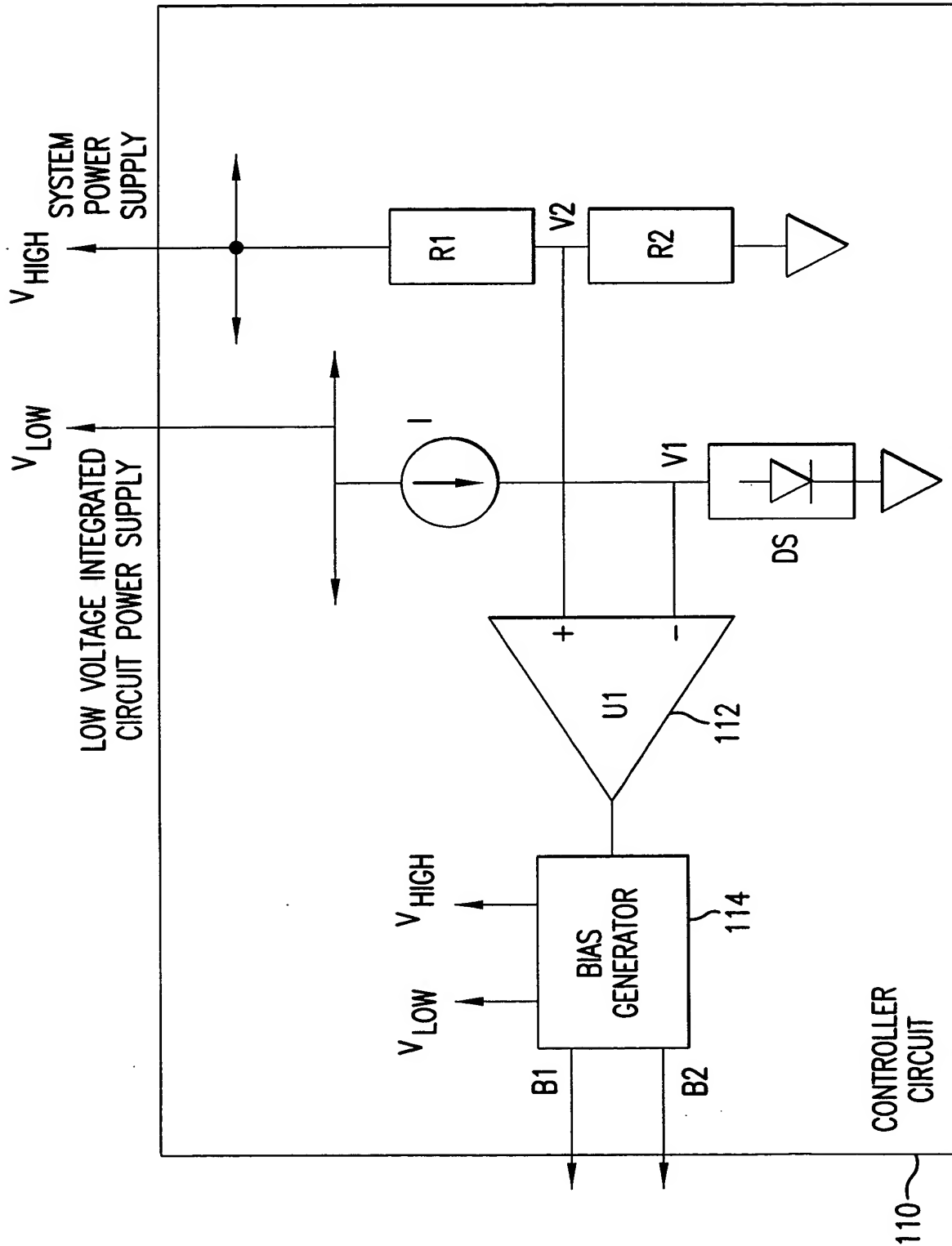


FIG.2

Appl. No. *To Be Assigned*; Filed: *Herewith*
 Dkt. No. 1875.0940003; Group Art Unit: *To Be Assigned*
 Inventor: Agnes N. Woo; Tel: 202/371-2600
 Title: System and Method for Independent Power
 Sequencing of Integrated Circuits





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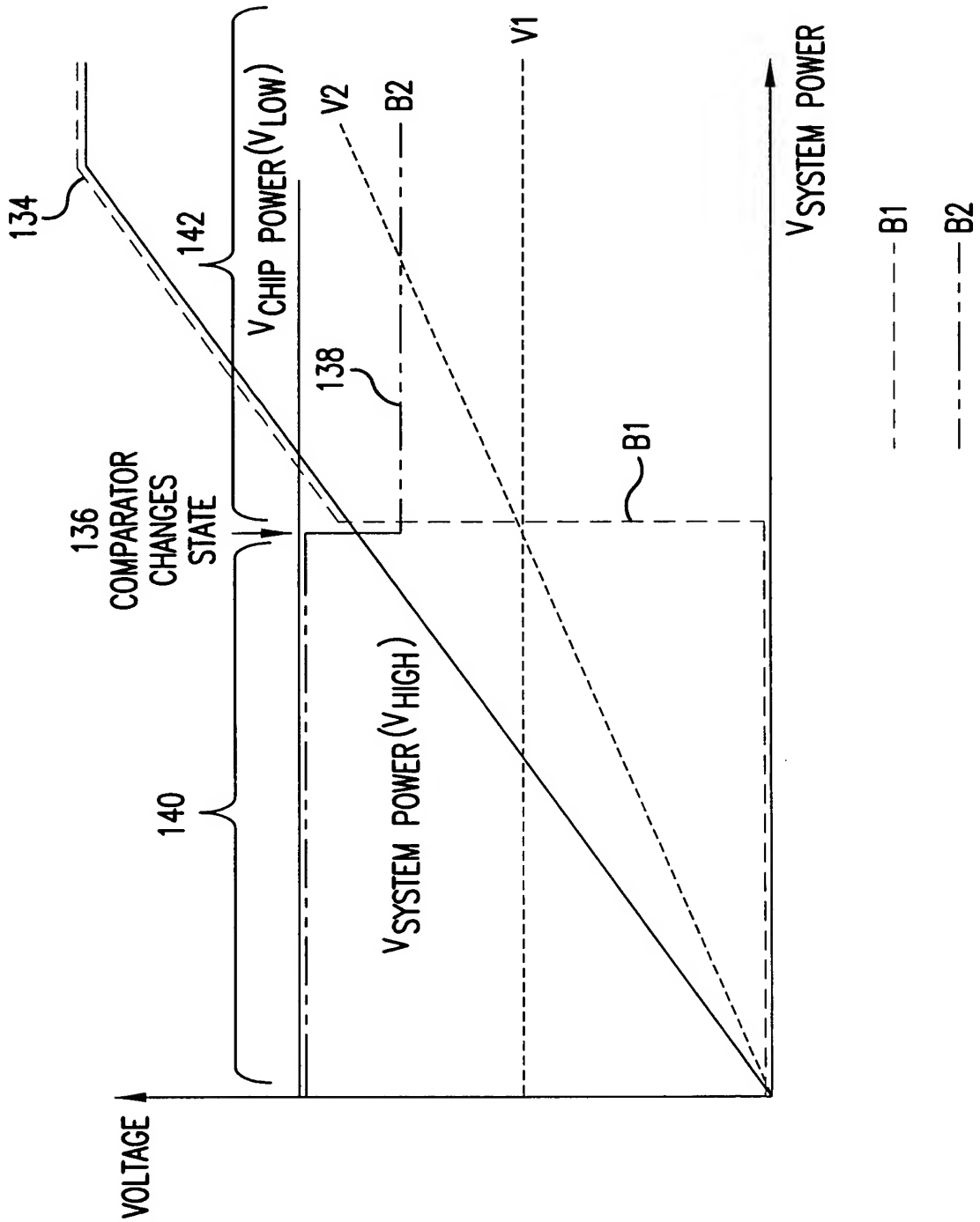


FIG. 5

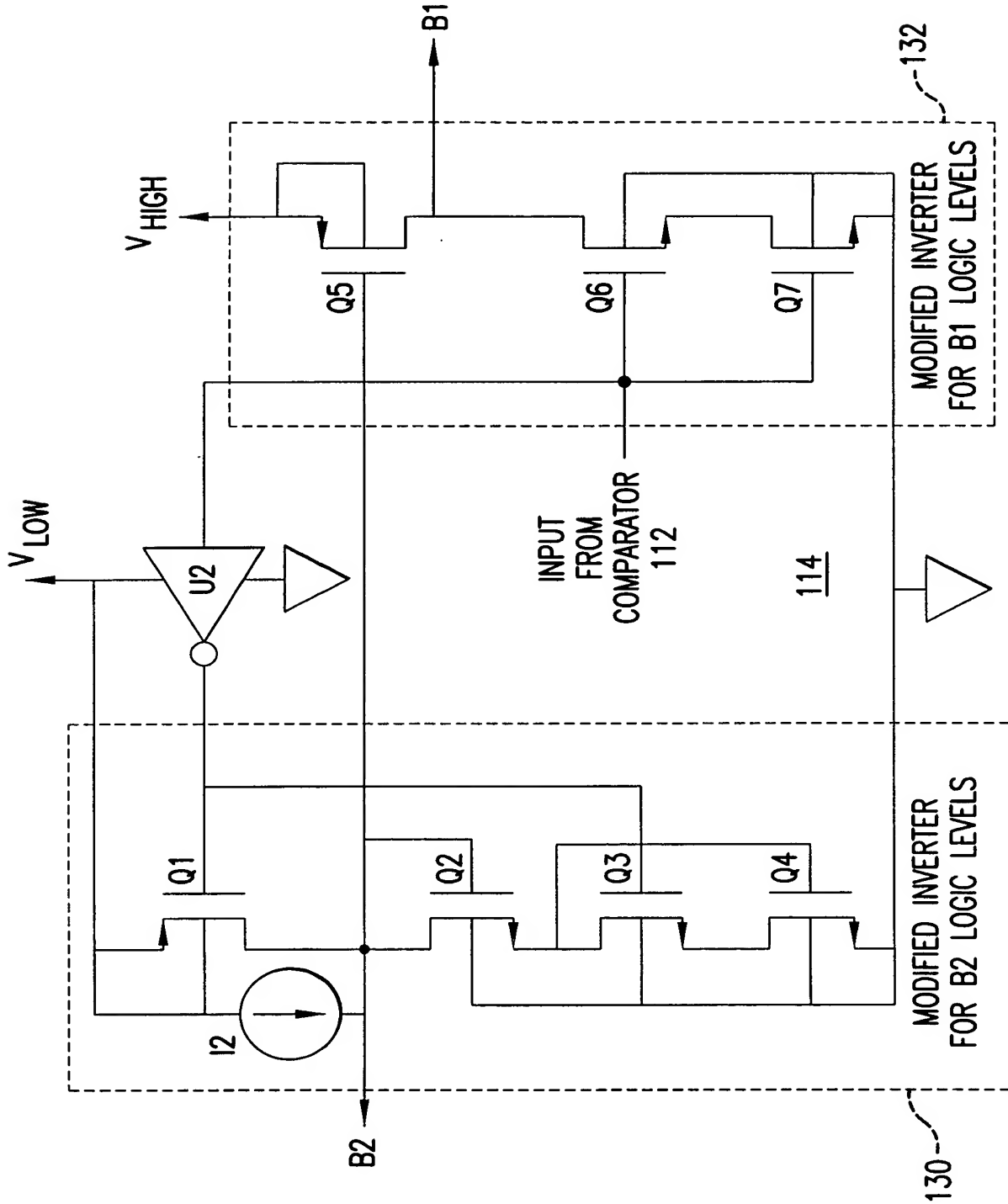


FIG. 6

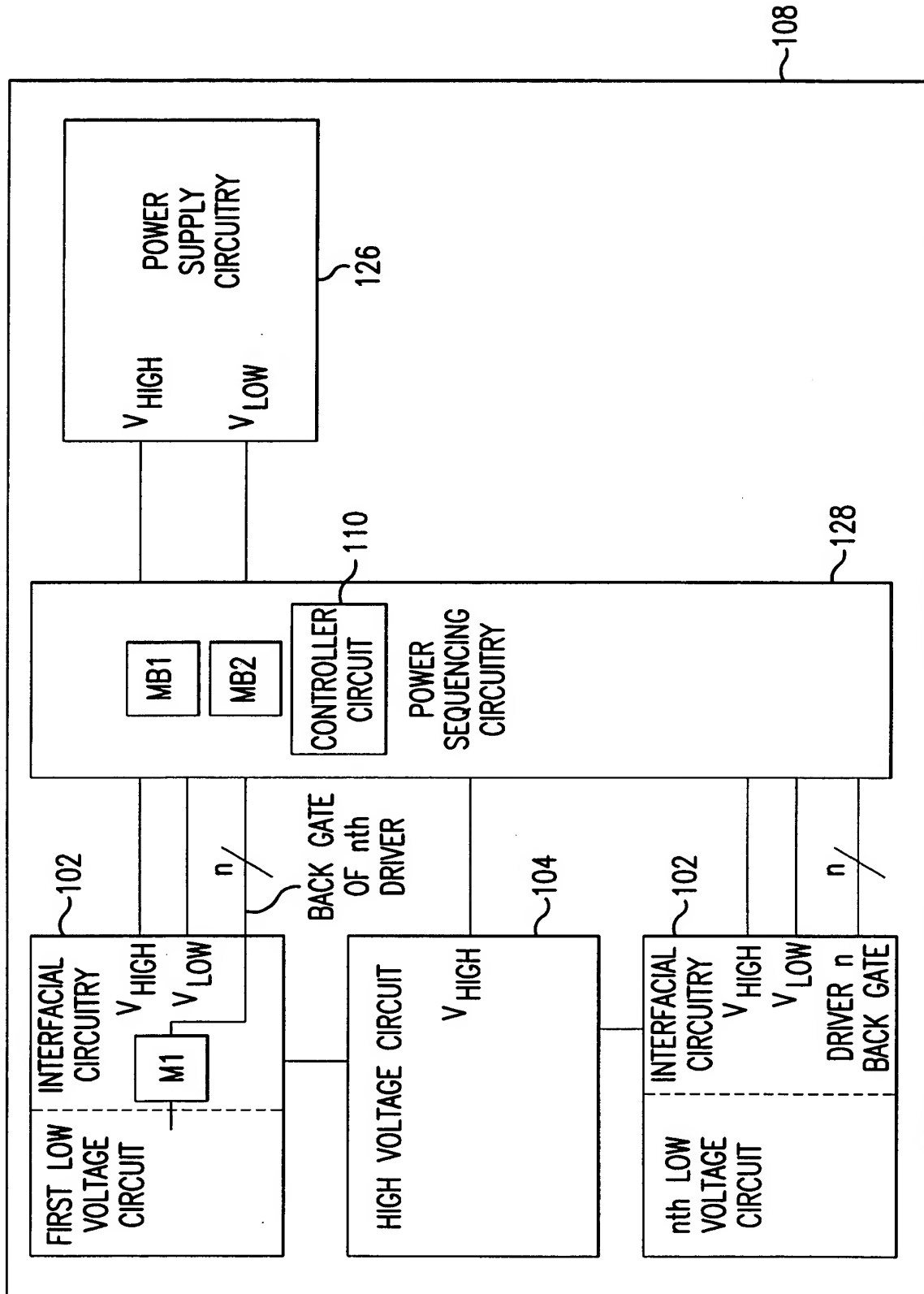


FIG. 7